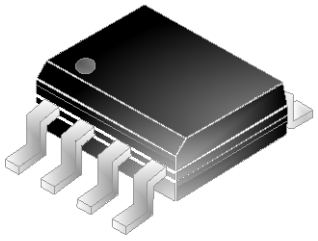
DW06-2PLC-S

Features

- 2000Watts peak pulse power (t_p=8/20μs)
- Protects Two Line Pairs (Four lines)
- Low capacitance
- Low leakage current
- Low operating and clamping voltage
- Solid-state Punch through Avalanche TVS process technology



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IEC Compatibility (EN61000-4)

- IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 100A (8/20μs)

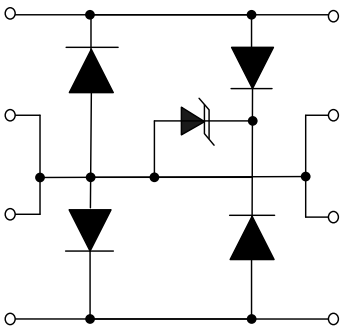
Mechanical Characteristics

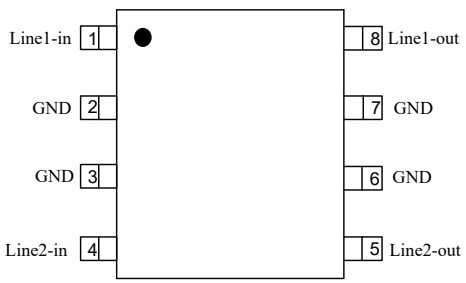
- JEDEC SO-8 package
- Molding compound flammability rating: UL 94V-0
- Marking: Marking Code
- Packaging: Tape and Reel
- RoHS Compliant

Applications

- T1/E1 Line Cards
- T3/E3 and DS3 Interfaces
- STS-1 Interfaces
- ISDN S/T-Interfaces
- ISDN U-Interfaces
- 10/100 Ethernet

Schematic & PIN Configuration



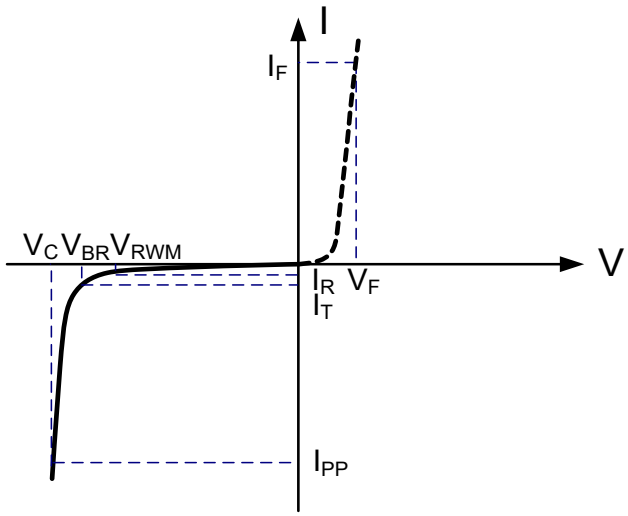


SO-8 (Top View)

Absolute Maximum Rating			
Rating	**Symbol**	**Value**	**Units**
Peak Pulse Power (t_p=8/20μs)	P_{PP}	2000	Watts
Peak Pulse Current (t_p=8/20μs)	I_{PP}	100	A
Lead Soldering Temperature	T_L	260(10sec)	℃
Operating Temperature	T_J	-55 to + 125	℃
Storage Temperature	T_{STG}	-55 to +150	℃

Electrical Parameters (T=25℃)

Symbol	Parameter
I_{PP}	Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_{PP}
V_{RWM}	Working Peak Reverse Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Breakdown Voltage @ I_T
I_T	Test Current
I_F	Forward Current
V_F	Forward Voltage @ I_F



Electrical Characteristics(T=25℃)

DW06-2PLC-S						
Parameter	**Symbol**	**Conditions**	**Minimum**	**Typical**	**Maximum**	**Units**
Reverse Stand-Off Voltage	V_{RWM}				6	V
Reverse Breakdown Voltage	V_{BR}	I_t=1mA	6.7			V
Reverse Leakage Current	I_R	V_{RWM}=6V,T=25℃			5	μA
Clamping Voltage	V_C	I_{pp}=100A Line to GND			20	V
Junction Capacitance	C_j	Between I/O pins and GND V_R=0V,f=1MHz		14	20	pF
		Between I/O pins V_R=0V,f=1MHz		7	10	pF

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Typical Characteristics

Figure 1: Peak Pulse Power vs. Pulse Time

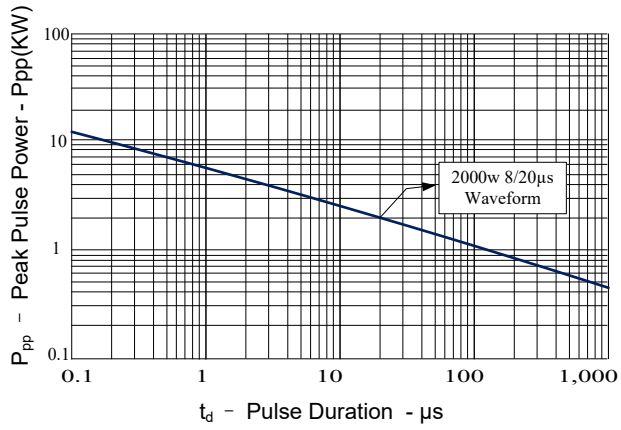


Figure 2: Power Derating Curve

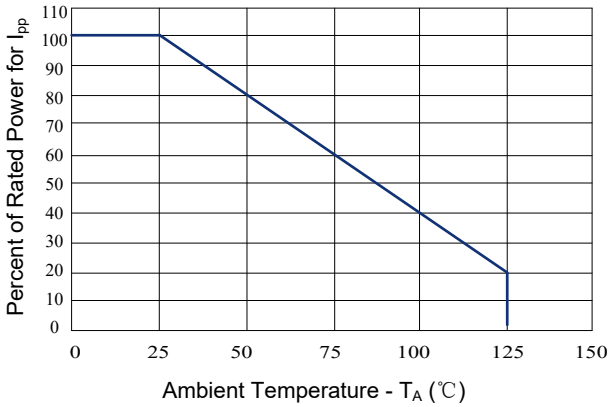


Figure3: Pulse Waveform

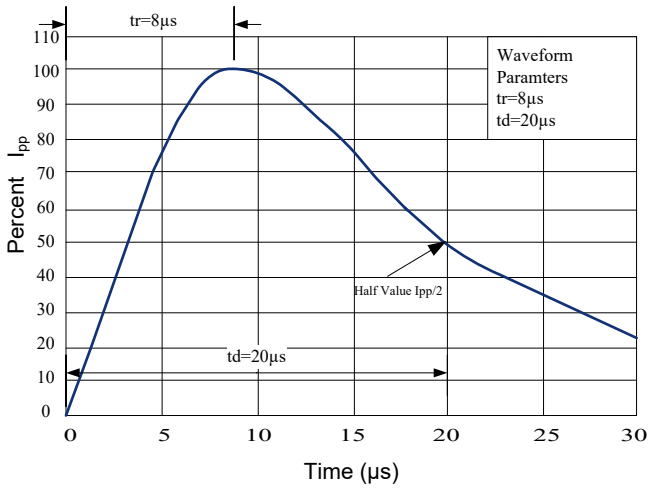


Figure 4: Clamping Voltage vs. Peak Pulse Current

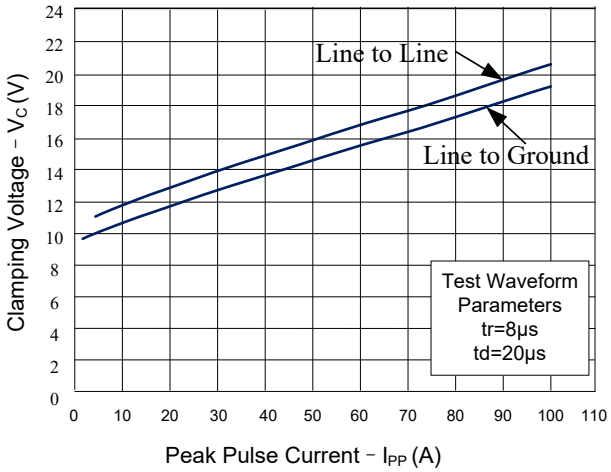


Figure 5: Capacitance vs. Reverse Voltage

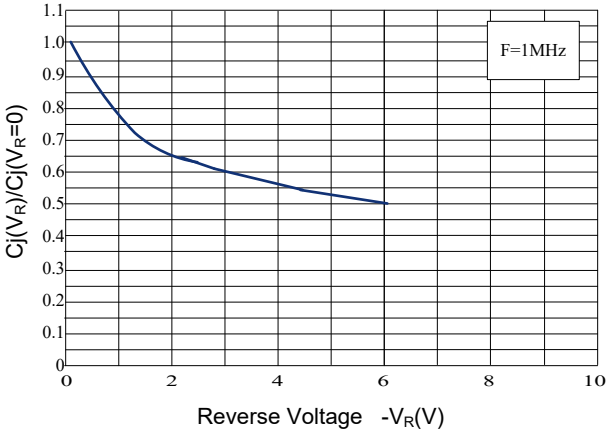
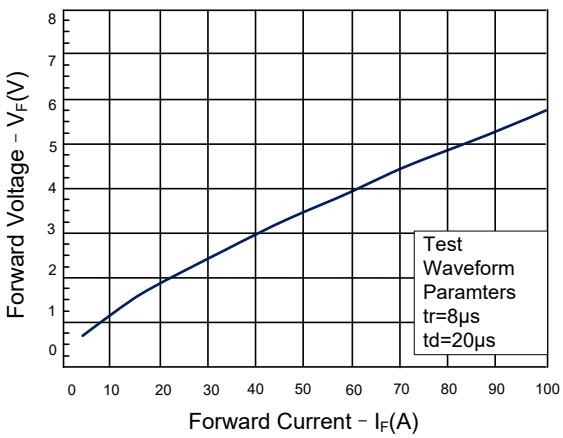
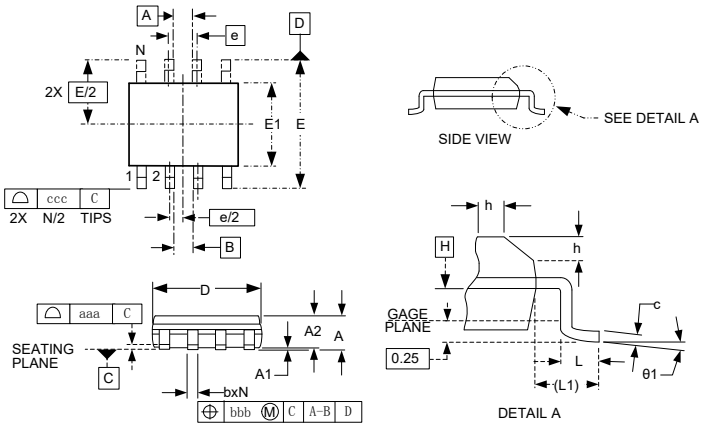


Figure 6: Forward Voltage vs. Forward Current



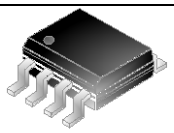
Outline Drawing – SO-8

PACKAGE OUTLINE



NOTES:

1. Controlling Dimensions Are In Millimeters (Angles In Degrees).
2. Datums -A- And -B- To Be Determined At Datum Plane -H-.
3. Dimensions "E1" And "D" Do Not Include Mold Flash,Protrusions Or Gate Burrs.
4. Reference JEDEC STD MS-012,VARITION AA.



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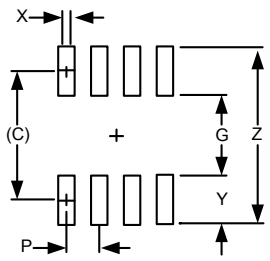
DIMENSIONS

DIM	INCHES			MILLIMETERS		
	MIN	NOM	MAX	MIN	NOM	MAX
A	.053	-	.069	1.35	-	1.75
A1	.004	-	.009	0.10	-	0.225
A2	.051	-	.060	1.30	-	1.50
b	.015	-	.019	0.39	-	0.48
c	.008	-	.010	0.21	-	0.26
D	.185	.193	.201	4.70	4.90	5.10
E1	.146	.154	.161	3.70	3.90	4.10
E	.236BSC			6.00BSC		
e	.050 BSC			1.27 BSC		
h	.010	-	.020	0.25	-	0.50
L	.020	.028	.031	0.50	0.72	0.80
θ 1	0°	-	8°	0°	-	8°
L1	(.041)			(1.04)		
N	8			8		
aaa	.004			0.10		
bbb	.010			0.25		
ccc	.008			0.20		



DIMENSIONS		
DIM	INCHES	MILLIMETERS
C	(.205)	(5.20)
G	.118	3.00
P	.050	1.27
X	.024	0.60
Y	.087	2.20
Z	.291	7.40

Notes

1. This Land Pattern Is For Reference Purposes Only.Consult Your Manufacturing Group To Ensure Your Company's Manufacturing Guidelines Are Met.
2. Reference IPC-SM-782A, RLP NO. 300A.

Marking Codes

Part Number	DW06-2PLC-S
Marking Code	PLC03-6B

Package Information

Qty：2.5k/Reel